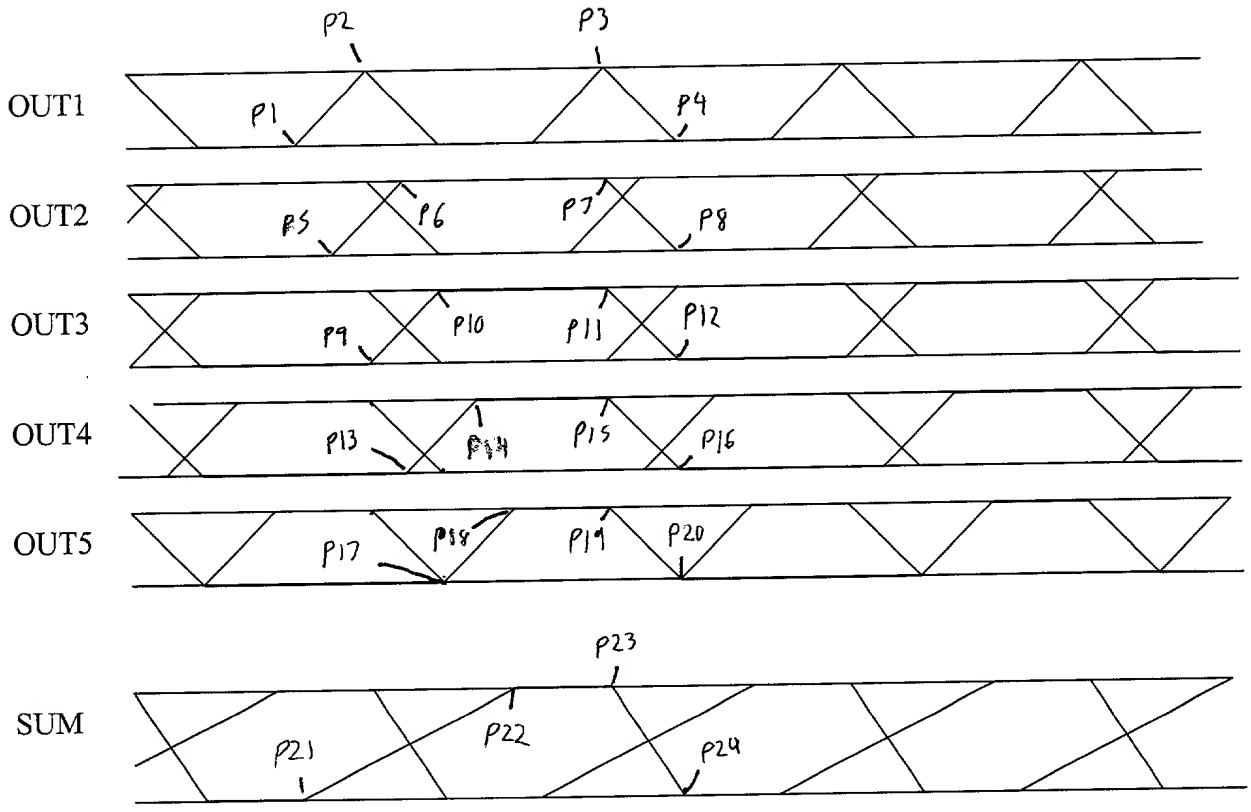

Figure 13

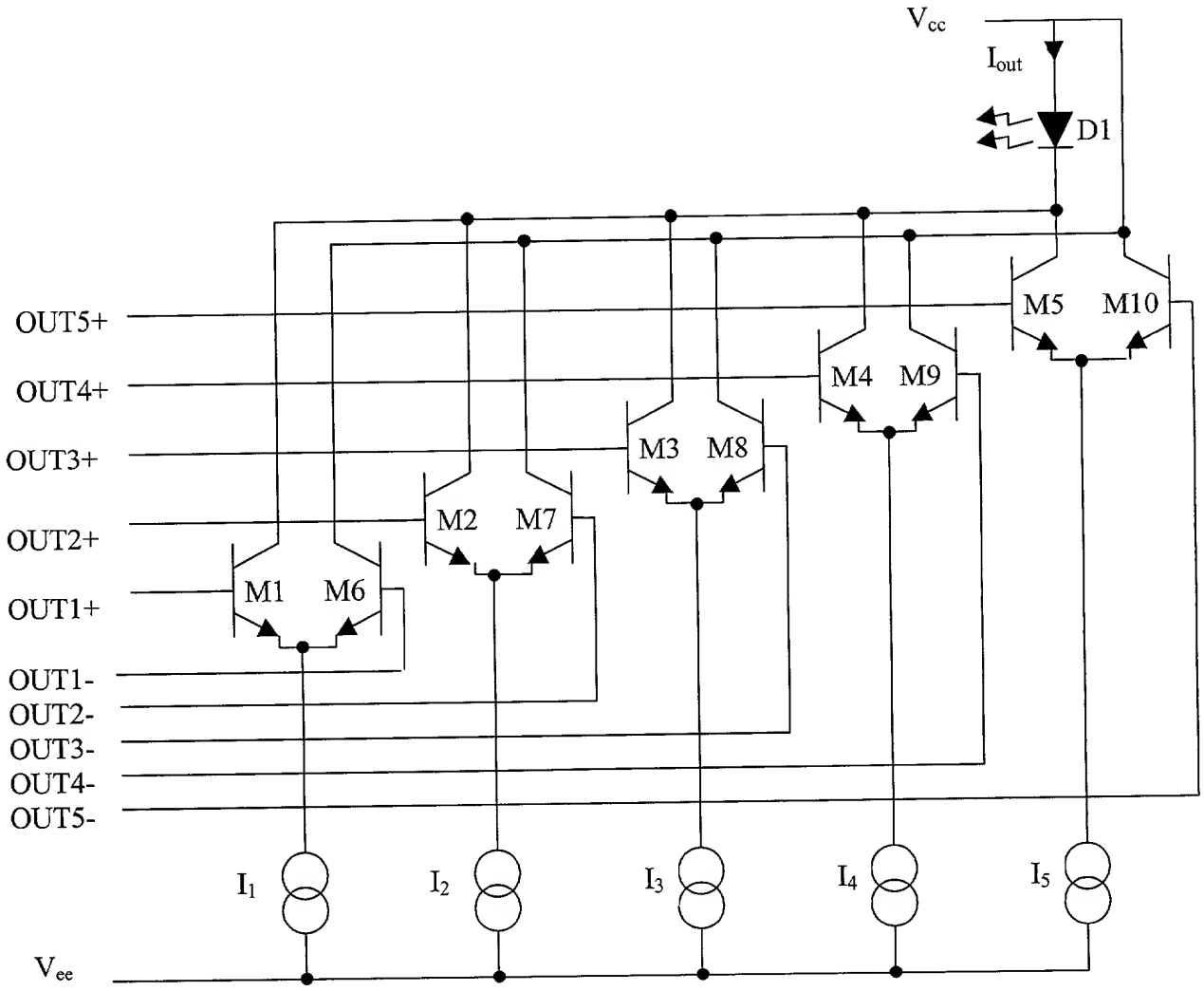
Vcc
Iout
D1
M5
M10
M4
M9
M3
M8
M2
M7
M1
M6
OUT5+
OUT4+
OUT3+
OUT2+
OUT1+
OUT1-
OUT2-
OUT3-
OUT4-
OUT5-
I1
I2
I3
I4
I5
Vee

Figure 14

Figure 15